

18-36V Input, 12V/17A Output Quarter Brick



Features

- High Efficiency: 92.8% at 12V/17A output
- Optimal thermal performance
- Low profile in standard footprint
- Wide input-voltage range: 16-40V
- Remote sense, On/Off control,
- Over-voltage, over-current, short-circuit, and over temperature protection
- Monotonic start-up into pre-biased load
- 1,500V isolation between input and output

For High Reliability Applications

Options

- Open-frame, Baseplate, or Encapsulated
- Auto-restart or lock-up protection mode
- Negative / Positive enable logic

Part Numbering System

QB	24	X	120	X	017	X	X	X	X	V
Series Name	Nominal Input Voltage	Temperature Grade (Baseplate Temperature)	Output Voltage	Enabling Logic	Rated Output Current	Pin Length	Electrical Option	Mechanical Options	Lead-free, ROHS Compliant	Suffix
QB	24: 18-36V	C: -40C--80C H: -40C--100C M: -55C--100C	Unit: 0.1V 120: 12.0V	P: Positive N: Negative	Unit: A 017:17A	R: 0.180"	0: Lock-up 2: Auto-restart	0: Open-frame 1: Baseplate 2: Encapsulated 3:with mounting holes	G: Lead-free	Output voltage: 20%-110%

General Specification

Absolute Maximum Rating

Excessive stresses over these absolute maximum ratings can cause permanent damage to the converter. Also, exposure to absolute maximum ratings for extended periods of time can adversely affect the reliability of the converter. Operation should be limited to the conditions outlined under the Electrical Specification Section.

Parameter	Symbol	Min	Max	Unit
Input Voltage (continuous)	V_i	-0.5	40	Vdc
Input Voltage (< 100ms, operating)	V_i	-	50	Vdc
Input Voltage (continuous, non-operating)	V_i	-	50	Vdc
I/O Isolation Voltage		-	1500	Vdc
Operating Ambient Temperature	T_o			
“C” Temperature grade		-40	80	°C
“H” Temperature grade		-40	100	°C
“M” Temperature grade		-55	100	°C
Storage Temperature	T_{stg}	-55	125	°C

Electrical Specifications

The specifications are valid over all operating conditions including input voltage, resistive load, and temperature except as noted.

Input Specifications

Parameter	Symbol	Min	Typical	Max	Unit
Input Voltage	V_i	16	24	40	Vdc
Input Current	I_{in_Max}	-	-	19	A
Quiescent Input Current (Typical V_{in})	I_{in_Qsnt}	-	150	200	mA
Standby Input Current	I_{in_Stdby}	-	6	10	mA
Inrush Transient	I^2t	-	-	0.1	A ² s
Input Reflected-ripple Current, Peak-to-peak (5 Hz to 20 MHz, 12 μ H source impedance)	-		10		mA
Input Ripple Rejection			-		dB
Input Turn-on Voltage Threshold	-	15.5	15.8	16	V
Input Turn-off Voltage Threshold	-	14.5	15	15.5	V
Input Voltage ON/OFF Hysteresis	-		1.5		V

Output Specifications

Parameter	Symbol	Min	Typical	Max	Unit
Output Voltage Set Point Accuracy (V_i = Typical V_{in} ; I_o = I_{o_max} ; T_a = 25°C)	-	-2		+2	% V_o
Output Voltage Set Point Accuracy (over all conditions)	-	-3		+3	% V_o
Output Regulation:					
Line Regulation (full range input voltage, 1/2 full load)	-	-	0.2	0.5	% V_o
Load Regulation (full range load, Typical V_{in})	-	-	0.2	0.5	% V_o
Temperature (T_a = -55°C to 100 °C)	-	-	0.1		% V_o
Output Current	I_o	0	-	17	A
Output Power	P_o	0		204	W
Efficiency (Typical V_{in} ; I_{o_max} , T_A = 25°C)	η	90.5	92.8	-	%
Output ripple frequency	-	220	250	230	kHz
Output Ripple and Noise Voltage RMS	-	-	-	80	mVrms
Peak-to-peak (5 Hz to 20 MHz bandwidth, Typical V_{in})				250	mVp-p
External Load Capacitance	-	-	-	14000	μ F

Output Specifications (continued)

Parameter	Symbol	Min	Typ	Max	Unit
Startup Delay, duration from enabling signal to V_o reaches 10% of its set point. (Typical V_{in} ; I_{o_max} , $T_A = 25^\circ\text{C}$)			1.5		ms
Startup Time, duration for V_o to rise from 10% of its set point to within its regulation band. (Typical V_{in} ; I_{o_max} , $T_A = 25^\circ\text{C}$)			3		ms
Output Over Current Protection Set Point / I_{o_max}		110	135	160	%
Output Over Voltage Protection Set Point / V_o typical		115	125	150	%
Output Remote Sense Range in % of V_o typical				10	%
Dynamic Response ($V_i = 36\text{V}$; $T_A = 25^\circ\text{C}$; Load transient $0.1\text{A}/\mu\text{s}$) Load steps from 50% to 75% of full load: Peak deviation Settling time (within 10% band of V_o deviation)			5 200		% V_o μs
Load step from 50% to 25% of full load Peak deviation Settling time (within 10% band of V_o deviation)			5 200		% V_o μs

General Specifications

Parameter	Symbol	Min	Typ	Max	Unit
Remote Enable Logic Low: $I_{ON/OFF} = 1.0\text{mA}$ $V_{ON/OFF} = 0.0\text{V}$	$V_{ON/OFF}$ $I_{ON/OFF}$	0 -	- -	1.2 1.0	V mA
Logic High: $I_{ON/OFF} = 0.0\mu\text{A}$ Leakage Current	$V_{ON/OFF}$ $I_{ON/OFF}$	3.5 -	- -	15 50	V μA
Over-temperature Protection set point	T_o	-	120	-	$^\circ\text{C}$
Isolation Capacitance	-	-	1200	-	pF
Isolation Resistance	-	10	-	-	M Ω
Calculated MTBF Telecordia SR-332, full load, 40°C , Typ. V_{in}			3.8		10^6 -hour
Calculated MTBF Telecordia SR-332, 50% load, 25°C , Typ. V_{in}			6.3		10^6 -hour

Feature Descriptions

The converter can be turned on and off by changing the voltage between the ON/OFF pin and Vin(-). The QB Series of converters is available with factory selectable positive logic and negative logic.

For the negative control logic, the converter is ON when the ON/OFF pin is at a logic low level and OFF when the ON/OFF pin is at a logic high level. For the positive control logic, the converter is ON when the ON/OFF pin is at a logic high level and OFF when the ON/OFF pin is at a logic low level.

With the internal pull-up circuitry, a simple external switch between the ON/OFF pin and Vin(-) can control the converter. A few example circuits for controlling the ON/OFF pin are shown in Fig. 1, 2 and 3.

The logic low level is from 0V to 1.2V and the maximum switch current during logic low is 1mA. The external switch must be capable of maintaining a logic-low level while sinking up to this current. The maximum voltage at the ON/OFF pin generated by the converter internal circuitry is less than 15V. The maximum allowable leakage current is 50μA.

Remote SENSE

The remote SENSE pins are used to sense the voltage at the load point to accurately regulate the load voltage and eliminate the impact of the voltage drop in the power distribution path.

SENSE(+) and SENSE(-) pins should be connected to the point where regulation is desired. The voltage between the SENSE pins and the output pins must not exceed 0.5V:

$$[V_{out}(+) - V_{out}(-)] - [SENSE(+) - SENSE(-)] < 0.5V$$

When remote sense is not used, the SENSE pins should be connected to their corresponding output terminals (positive and negative). If the SENSE pins are left floating, the converter will deliver an output voltage slightly higher than its specified typical output voltage. Since the OVP (output over-voltage protection) circuit senses the voltage across the output pins (Pin 8 and Pin 4), the total voltage rise should not exceed the minimum OVP setpoint given in the Specifications table.

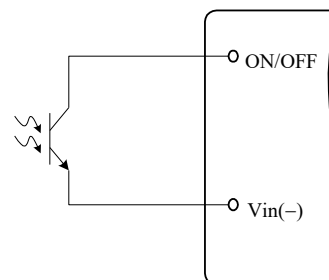


Fig. 1 Opto Coupler Enable Circuit

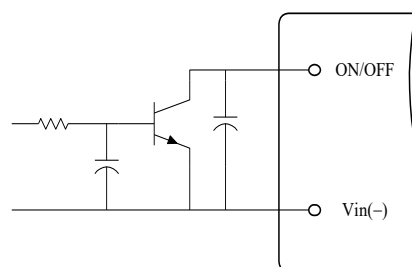


Fig. 2 Open Collector Enable Circuit

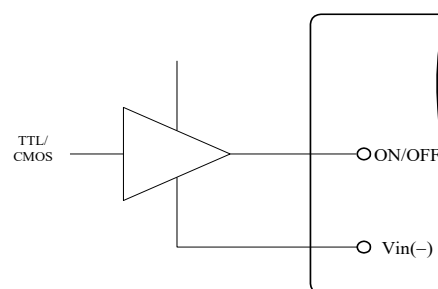


Fig. 3 Direct Logic Drive

Input Under-Voltage Lockout

This feature prevents the converter from starting until the input voltage reaches the turn-on voltage threshold, and keeps the converter running until the input voltage falls below the turn-off voltage threshold. Both turn-on and turn-off voltage thresholds are defined in the Input Specifications table. The hysteresis prevents oscillations.

Output Over-Current Protection

As a standard feature, the converter will latch off when the load current exceeds the current limit. The converter can be restarted by toggling the ON/OFF switch or recycling the input voltage. With the auto-restart option, the converter will operate in a hiccup mode (repeatedly try to restart) until the over-current condition is cleared.

Output Over-Voltage Protection

If the voltage across the output pins exceeds the output voltage protection threshold as given in the Specifications table, the converter will shut down to protect the converter and the load.

As a standard feature, the converter will shut down and latch off when this fault occurs. The converter can be restarted by toggling the ON/OFF switch or recycling the input voltage. With the auto-restart option, the converter will operate in a hiccup mode until the over-voltage cause is cleared.

Thermal Shutdown

As a standard feature, the converter will shut down and latch off if an over-temperature condition is detected. The converter has a temperature sensor located at a carefully selected position in the converter circuit board, which represents the thermal condition of key components of the converter.

The thermal shutdown circuit is designed to turn the converter off when the temperature at the sensor reaches 120°C. The module can be restarted by toggling the ON/OFF switch or recycling the input voltage. With the auto-restart option, the converter will resume operation after the converter cools down.

Design Considerations

Input Source Impedance

As with any DC/DC converter, the stability of the QB converters may be compromised if the source impedance is too high or inductive. It's desirable to keep the input source ac-impedance as low as possible. Although the converters are designed to be stable without an additional input capacitor for typical source impedance, it is recommended to use at least a 220—

470 μ F low ESR electrolytic capacitor at the input of the converter to reduce the potential impact of the source impedance. This electrolytic capacitor should have sufficient RMS current rating over the operating temperature range.

Safety Considerations

The QB Series of converters are designed in accordance with EN 60950 Safety of Information Technology Equipment Including Electrical Equipment. The converters are recognized by UL in both USA and Canada to meet 1500V Basic Insulation requirements in UL 60950, Safety of Information Technology Equipment and applicable Canadian Safety Requirement, and ULc 60950. Flammability ratings of the PWB and plastic components in the converter meet 94V-0.

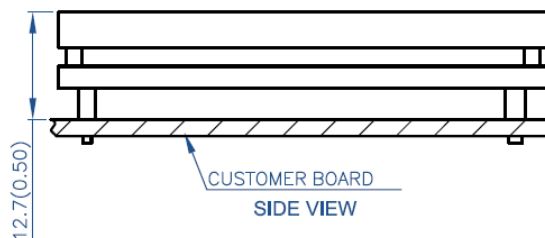
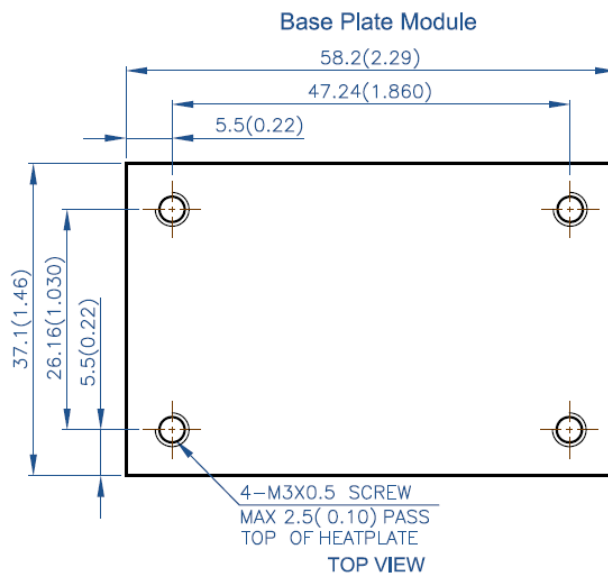
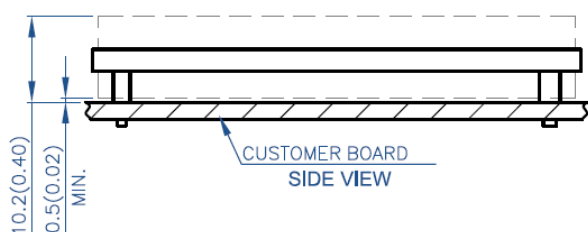
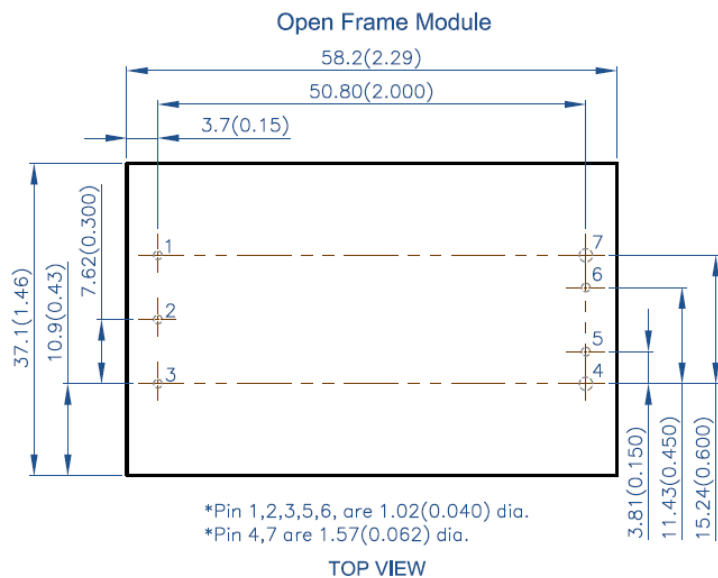
To protect the converter and the system, an input line fuse is highly recommended on the un-grounded input end.

Thermal Considerations

The QB Series of converters can operate in various thermal environments. Due to the high efficiency and optimal heat distribution, these converters exhibit excellent thermal performance. Proper cooling can be verified by monitoring the temperature of the case not continuously exceeding 120 °C.

The maximum allowable output power of any power converter is usually determined by the electrical design and the maximum operating temperature of its components.

Mechanical Diagrams

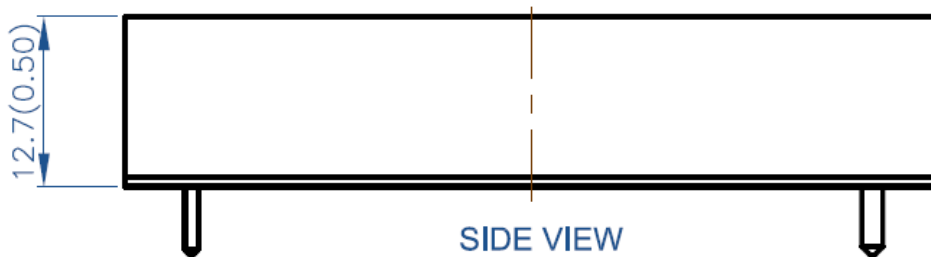
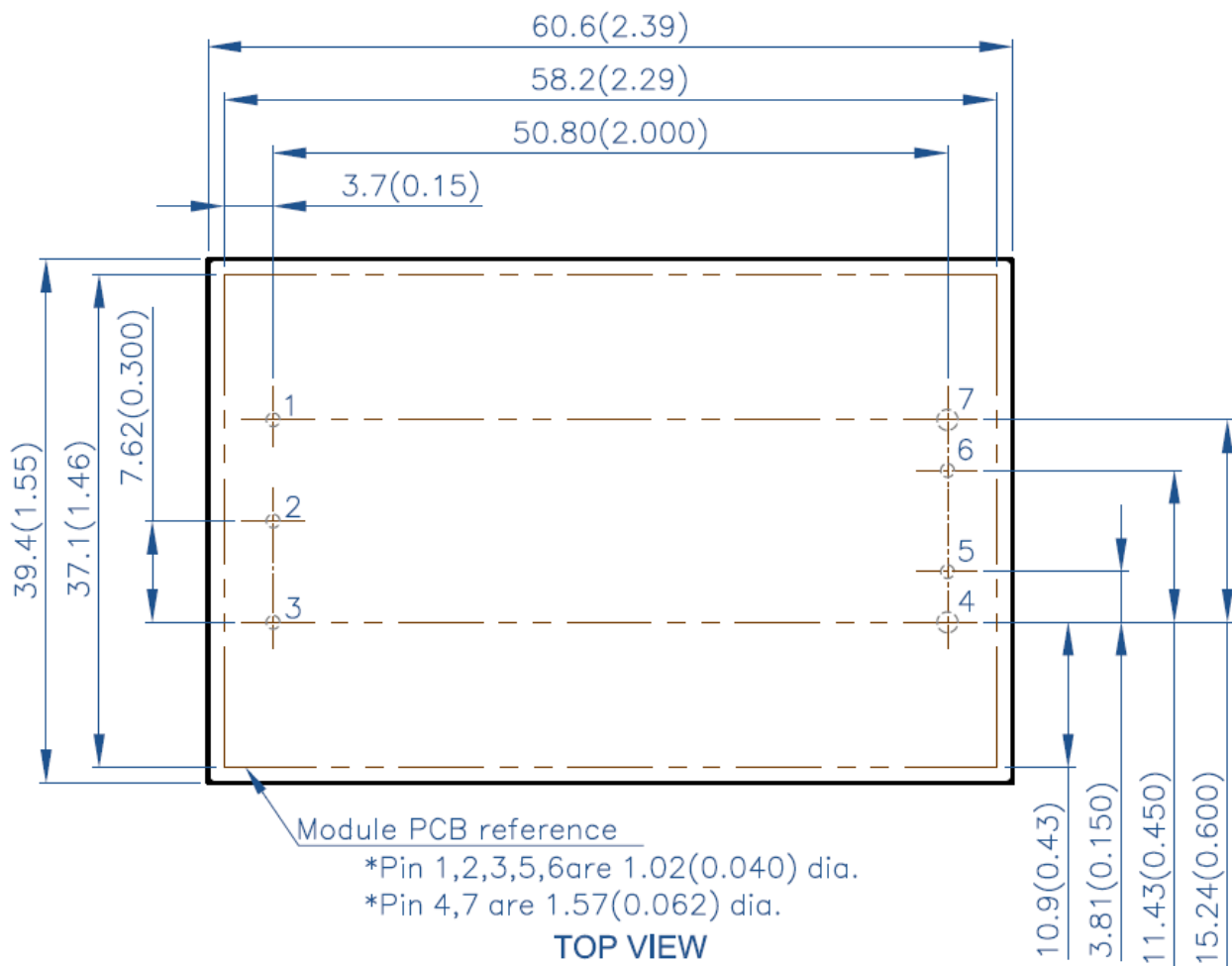


Pin	Name	Function
1	Vin(+)	Input voltage positive
2	ON/OFF	Remote control
3	Vin(-)	Input voltage negative
4	Vout(-)	Output voltage negative
5	SENSE(-)	Negative remote sense
6	SENSE(+)	Positive remote sense
7	Vout(+)	Output voltage positive

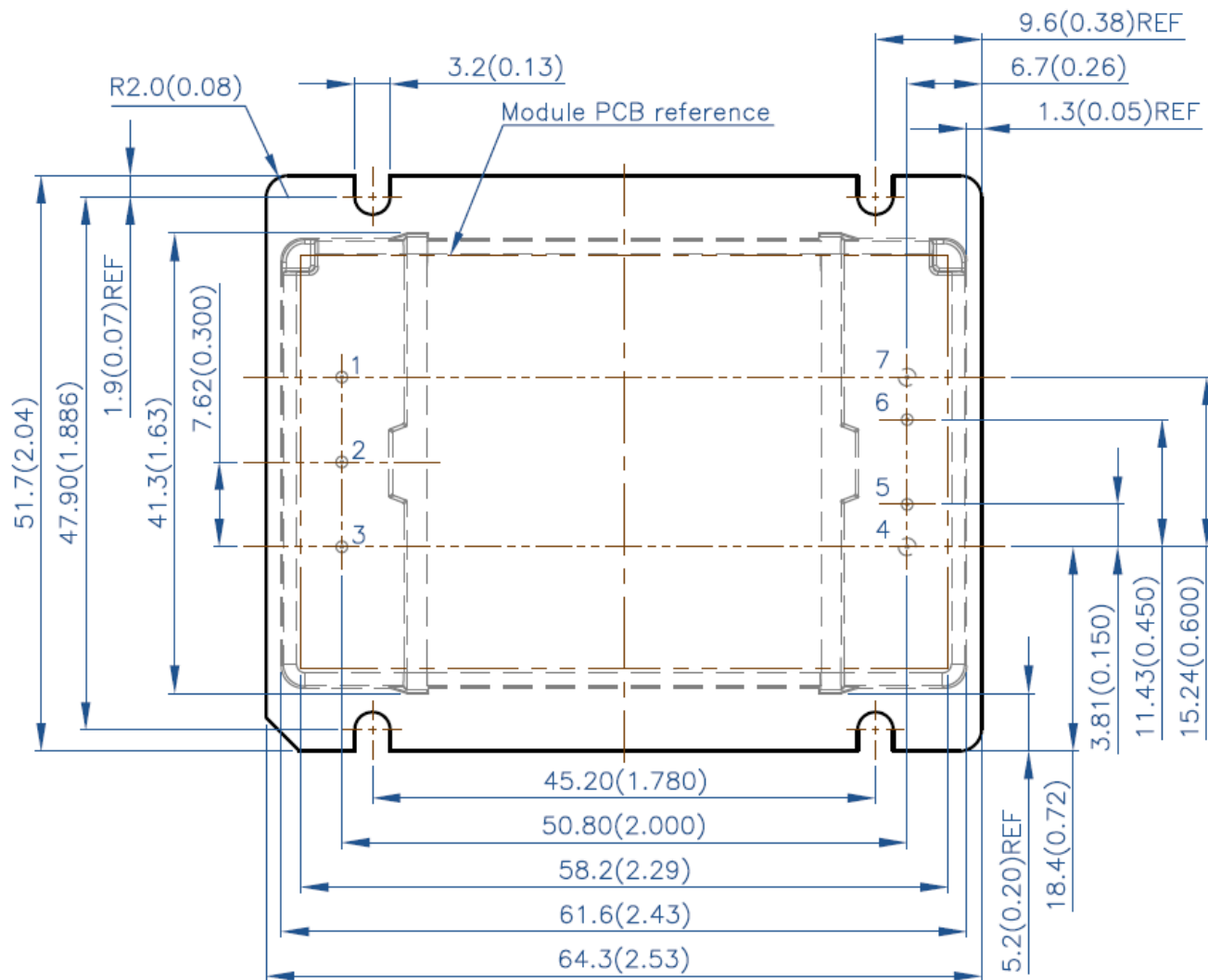
Notes:

All dimensions in mm (inches)
Tolerances: .x $\pm$.5 (.xx $\pm$ 0.02)
.xx $\pm$.25 (.xxx $\pm$ 0.010)

**Encapsulated Type-I
(without mounting holes)**



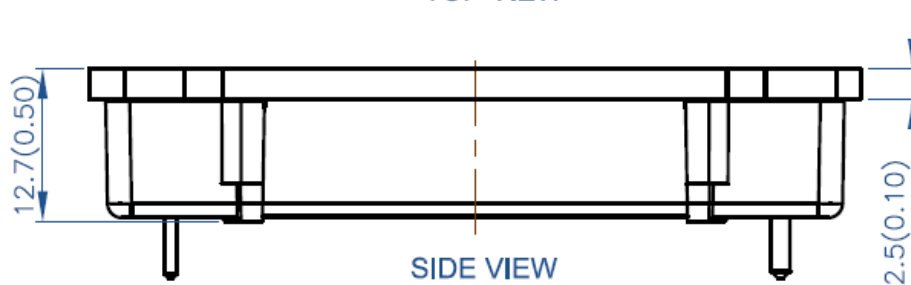
Encapsulated Type- II
(with mounting holes)



*Pin 1,2,3,5,6, are 1.02(0.040) dia.

*Pin 4,7 are 1.57(0.062) dia.

TOP VIEW



SIDE VIEW



QB24x120x017xxxC 18-36V Input,12V/17A Output DC/DC Converter

**For more information, please contact
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Warranty

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